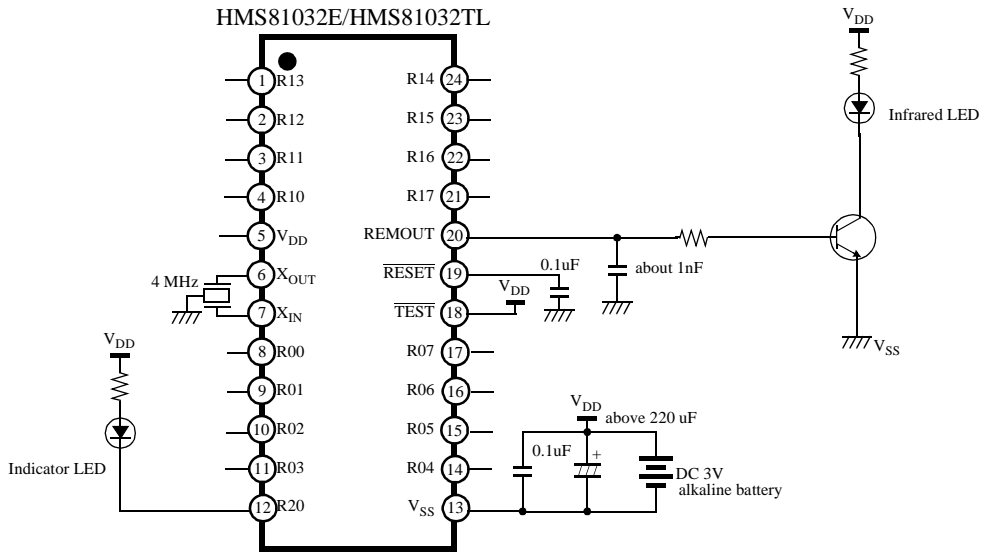
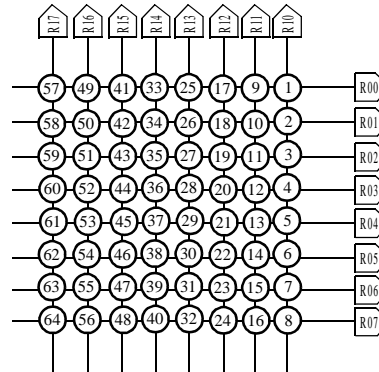


## Recommendable circuit diagram of HMS81032E/HMS81032TL

In case of using high gain Tr. for longer transmission distance application we recommend to attach proper value of capacitor between REMOUT pin of chip and  $V_{SS}$  of the system to prevent excessive overshoot voltage of the system power (over the maximum supply  $V_{DD}$  of chip) during signal transmission.

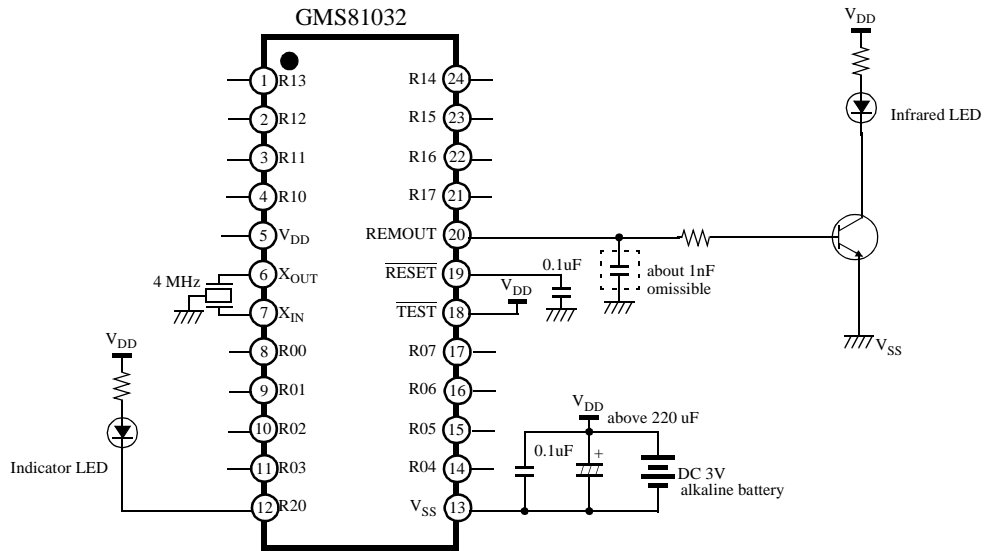


1. Tr. and IR LED should be located as far as possible from the IC.
2. Power capacitor and noise filtering capacitor should be located as near as possible from the IC.
3. Ceramic resonator should be located as near as possible from the IC.



## Recommendable circuit diagram of GMS81032

In case of using high gain Tr. for longer transmission distance application we recommend to attach proper value of capacitor between REMOUT pin of chip and  $V_{SS}$  of the system to prevent excessive overshoot voltage of the system power (over the maximum supply  $V_{DD}$  of chip) during signal transmission.



1. Tr. and IR LED should be located as far as possible from the IC.
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