

Factory Programming Order & VERIFICATION SHEET

MC40P5004

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<input type="text"/>	<input type="text"/>	<input type="text"/>
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Please fill in the bold areas.

1. Customer Information

Company Name	<input type="text"/>
Application	<input type="text"/>
Order Date	YYYY MM DD
Tel :	<input type="text"/>
Fax :	<input type="text"/>
Name & : Signature	<input type="text"/>

2. Device Information

Package	<input type="checkbox"/> 20 SOP(D) <input type="checkbox"/> 20 TSSOP(R)
	<input type="checkbox"/> Wafer Chip sales code:()
ROM Code Size	<input type="checkbox"/> 1 K byte
	<input type="checkbox"/> 2 K byte
	<input type="checkbox"/> 4 K byte

4. Writing Option

<input type="radio"/> fosc/48 (Default) <input type="radio"/> fosc/12
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3. OTP file Information

Check Sum	<input type="text"/>
File Name	<input type="text"/> .RHX
You must fill unused area with 0xFF	
0000 _H	1K
03FF _H	2K
07FF _H	4K
0FFF _H	<input type="text"/>

5. ROM CODE Verification

Verification Date :	<input type="text"/>
Check Sum :	<input type="text"/>
Name:	<input type="text"/>

6. Remark

Marking : standard

Factory Programming Order & VERIFICATION SHEET

MC40P5204

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Please fill in the bold areas.

1. Customer Information

Company Name	
Application	
Order Date	YYYY MM DD
Tel :	
Fax :	
Name & : Signature	

2. Device Information

Package	<input type="checkbox"/> 24 SOP(D) <input type="checkbox"/> 24 TSSOP(R) <input type="checkbox"/> Wafer Chip sales code:()
ROM Code Size	<input type="checkbox"/> 1 K byte <input type="checkbox"/> 2 K byte <input type="checkbox"/> 4 K byte

4. Writing Option

fosc/48 (Default)
 fosc/12

3. OTP file Information

Check Sum	
File Name	.RHX
You must fill unused area with 0xFF	
0000 _H	1K
03FF _H	2K
07FF _H	4K
0FFF _H	

5. ROM CODE Verification

Verification Date :
Check Sum :
Name:

6. Remark

Marking : standard

Factory Programming Order & VERIFICATION SHEET

MC40P5404

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Please fill in the bold areas.

1. Customer Information

Company Name	
Application	
Order Date	YYYY MM DD
Tel :	
Fax :	
Name & : Signature	

2. Device Information

Package	<input type="checkbox"/> 20 SOP(D) <input type="checkbox"/> 20 TSSOP(R) <input type="checkbox"/> Wafer Chip sales code:()
ROM Code Size	<input type="checkbox"/> 1 K byte <input type="checkbox"/> 2 K byte <input type="checkbox"/> 4 K byte

4. Writing Option

fosc/48 (Default)
 fosc/12

3. OTP file Information

Check Sum	
File Name	.RHX
You must fill unused area with 0xFF	
0000 _H	1K
03FF _H	2K
07FF _H	4K
0FFF _H	

5. ROM CODE Verification

Verification Date :
Check Sum :
Name:

6. Remark

Marking : standard
