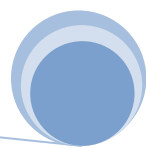


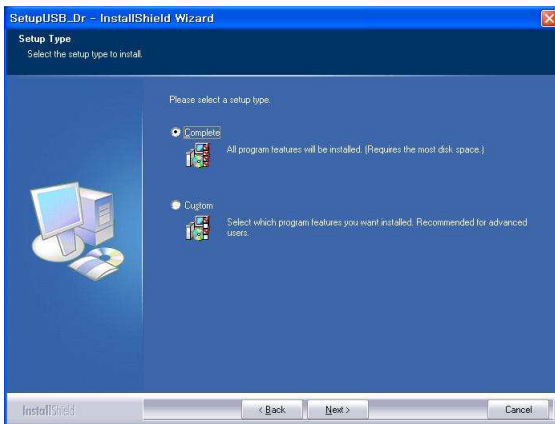
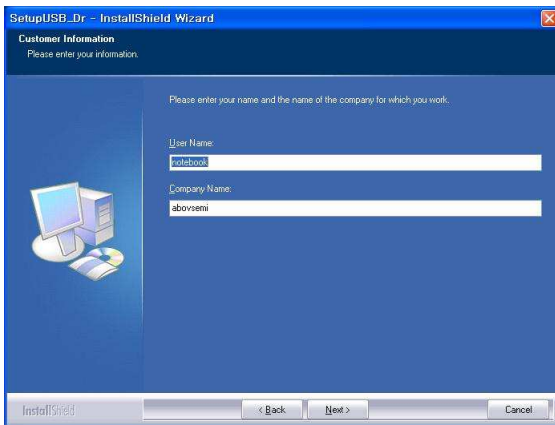
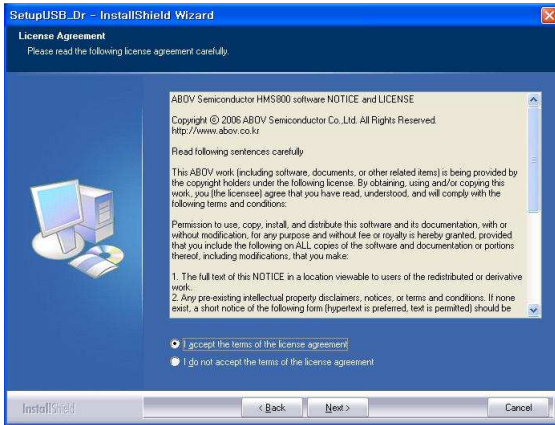


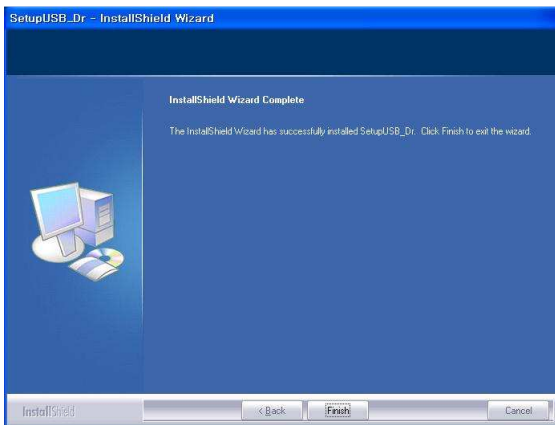
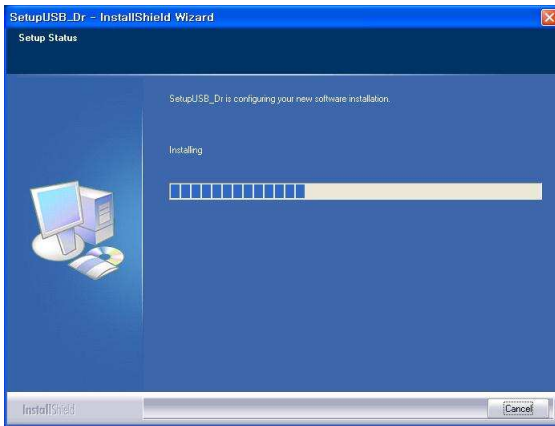
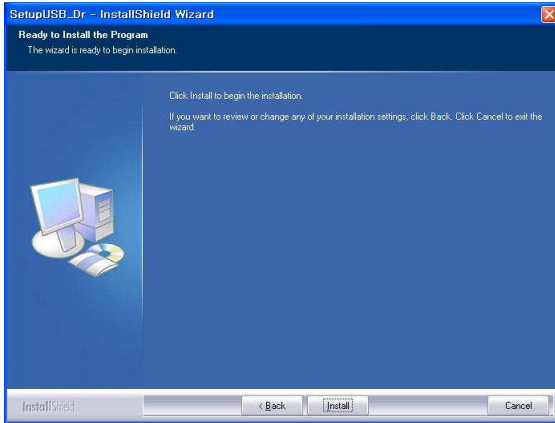
ABOV
CodeGen32
(Code Generator)
USER GUIDE

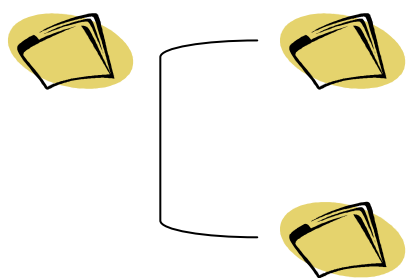
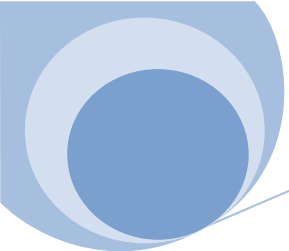












ABOV 32bit : A3x series code generator

AC33M8128 : 80 MQFP (project file = ac33m8128_80mq.VPR)

Peripherals: Clock, GPIO-A, GPIO-B, GPIO-C, GPIO-D, ADC0, ADC1, ADC2, I2C0, I2C1 Conflict GPIO-D, MPWM0 Conflict GPIO-B, MPWM1 Conflict GPIO-B, SPI0 Conflict GPIO-A, SPI1 Conflict GPIO-D, TIMER0, TIMER1, TIMER2, TIMER3, TIMER8, TIMER9 Conflict itself, UART0 Conflict GPIO-C, UART1, UART2 Conflict GPIO-D, UART3, WDT

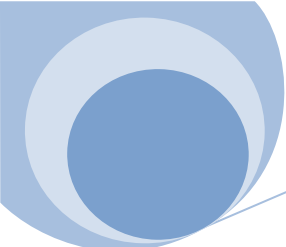
Properties: Application, Internal OSC (SOSC 1MHz, IOSC 20MHz), External OSC (MOSC), PLL OSC (PLL input MOSC, Xin 8.000000), MCLK OSC (MCLK input PLL output, MCLK 8.000000), System clock (Clock input IOSC, Clock divisor 2, Clock 5.000000), ADC clock (Clock input IOSC, Clock divisor 2, Clock 5.000000)

Package: Pin list including VDD, GND, MoSo1/PD2, MaSo1/PD3, COMP0/AN0/EA0, COMP1/AN1/EA1, COMP2/AN2/EA2, COMP3/AN3/EA3, T0o/AN4/EA4, T1o/AN5/EA5, CREP0/T2o/AN6/EA6, TRACE03/CREP1/T3o/AN7/EA7, AGND, AVDD, TRACECLK/AD0o/AN8/EA8, PC3/TD1, PC2/TDo/SWo, GND, VDD, PD13/AD1EoC/T3C, PD12/AD1S-oC/T2C/PH2, JMI, PD11/AD0EoC/T1C/PHB, PD10/AD0S-oC/T0C/PHA, PC1/TMS/SWD1o, PC0/TCR/SWDCCLK, VDD, GND, PB15/MP1WL, PB14/MP1WH, PB13/MP1VL, PB12/MP1VH

Output: Message count is 28. I2C1 : SDA1 is not selected. I2C1 : SCL1 is not selected. MPWM0 : MPROVH is not selected. MPWM0 : MPOVL is not selected. MPWM0 : MPOVH is not selected. MPWM0 : PRTINO is not selected. MPWM0 : OVINO is not selected. MPWM1 : MP11H is not selected.

Pane 1 | Error | Pane 2





ABOV 32bit C source generator : Project selection Dialog

LAST project No project file found

NEW project

32bit

Series name AC33M,...


Device name AC33M6128L

Package type AC33M6128L
AC33M8128
AC33M8128L

Project name

Load project

Delete project



V1.000.00 (2016. 4.22)

Programmed by Seungduk Ha

Cancel

ABOV 32bit C source generator : Project selection Dialog

LAST project No project file found

NEW project

32bit

Series name AC33M,...


Device name AC33M6128L

Package type 64 LQFP

Project name 64 LQFP

Load project

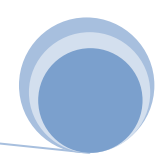
Delete project



V1.000.00 (2016. 4.22)

Programmed by Seungduk Ha

Cancel





ABOV 32bit : A3x series code generator

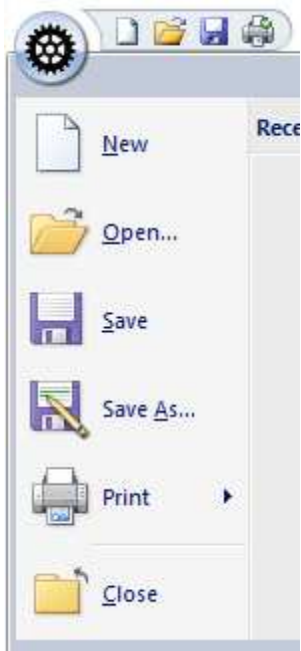
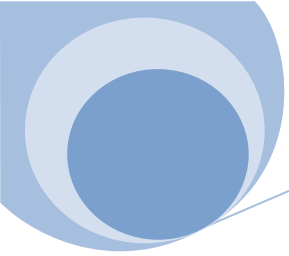
AC33M8128 : 80 MQFP (project file = ac33m8128_80mq.VPR)

Peripherals: Clock, GPIO-A, GPIO-B, GPIO-C, GPIO-D, ADC0, ADC1, ADC2, I2C0, I2C1 Conflict GPIO-D, MPWM0 Conflict GPIO-B, MPWM1 Conflict GPIO-B, SPI0 Conflict GPIO-A, SPI1 Conflict GPIO-D, TIMER0, TIMER1, TIMER2, TIMER3, TIMER8, TIMER9 Conflict itself, UART0 Conflict GPIO-C, UART1, UART2 Conflict GPIO-D, UART3, WDT

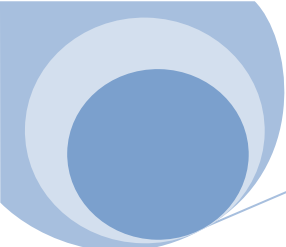
Application Properties: Internal OSC (SOSC 1MHz, IOSC 20MHz), External OSC (MOSC), PLL OSC (PLL, PLL input, PLL (MHz)), MCLK OSC (MCLK input, MCLK (MHz)), System clock (Clock input, Clock divider), OSC

Package Pinout:

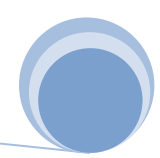
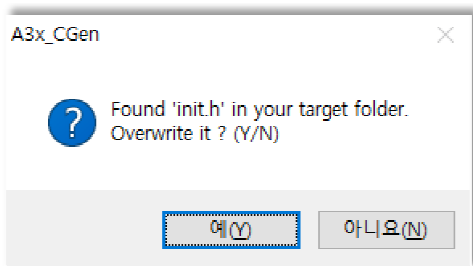
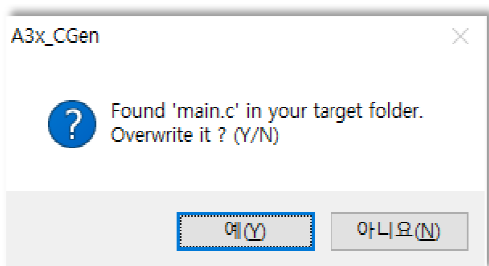
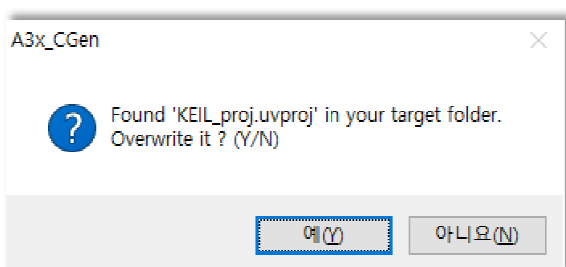
1	VDD	PC3/TDI
2	GND	PC2/TDo/SwO
3	GND	GND
4	VDD	VDD
5	MoSo1/SD2	PD13/AD1EoC/T3C
6	MoSo1/SD3	PD12/AD1SoC/T2C/BH2
7	COMP0/AN0/EA0	PD11/AD0EoC/T1C/BH1
8	COMP1/AN1/EA1	PD10/AD0SoC/T0C/PHA
9	COMP2/AN2/EA2	PD1/TMS/SWDIo
10	COMP3/AN3/EA3	PD0/TCR/SWDCLK
11	TIO/AN4/EA4	VDD
12	TIO/AN5/EA5	GND
13	TRACED3/CREP1/T3o/AN7/EA7	PD15/MP1WL
14	AGND	PD14/MP1WH
15	AVDD	PD13/MP1VL
16	TRACELK/AD0o/AN8/EA8	PD12/MP1VH
17	TRACED3/AN1/AN2/AN3/AN4/AN5/AN6/AN7/AN8/AN9/AN10/AN11/AN12/AN13/AN14/AN15/AN16/AN17/AN18/AN19/AN20/AN21/AN22/AN23/AN24/AN25/AN26/AN27/AN28/AN29/AN30/AN31/AN32/AN33/AN34/AN35/AN36/AN37/AN38/AN39/AN40/AN41/AN42/AN43/AN44/AN45/AN46/AN47/AN48/AN49/AN50/AN51/AN52/AN53/AN54/AN55/AN56/AN57/AN58/AN59/AN60/AN61/AN62/AN63/AN64/AN65/AN66/AN67/AN68/AN69/AN70/AN71/AN72/AN73/AN74/AN75/AN76/AN77/AN78/AN79/AN80/AN81/AN82/AN83/AN84/AN85/AN86/AN87/AN88/AN89/AN90/AN91/AN92/AN93/AN94/AN95/AN96/AN97/AN98/AN99/AN100/AN101/AN102/AN103/AN104/AN105/AN106/AN107/AN108/AN109/AN110/AN111/AN112/AN113/AN114/AN115/AN116/AN117/AN118/AN119/AN120/AN121/AN122/AN123/AN124/AN125/AN126/AN127/AN128/AN129/AN130/AN131/AN132/AN133/AN134/AN135/AN136/AN137/AN138/AN139/AN140/AN141/AN142/AN143/AN144/AN145/AN146/AN147/AN148/AN149/AN150/AN151/AN152/AN153/AN154/AN155/AN156/AN157/AN158/AN159/AN160/AN161/AN162/AN163/AN164/AN165/AN166/AN167/AN168/AN169/AN170/AN171/AN172/AN173/AN174/AN175/AN176/AN177/AN178/AN179/AN180/AN181/AN182/AN183/AN184/AN185/AN186/AN187/AN188/AN189/AN190/AN191/AN192/AN193/AN194/AN195/AN196/AN197/AN198/AN199/AN200/AN201/AN202/AN203/AN204/AN205/AN206/AN207/AN208/AN209/AN210/AN211/AN212/AN213/AN214/AN215/AN216/AN217/AN218/AN219/AN220/AN221/AN222/AN223/AN224/AN225/AN226/AN227/AN228/AN229/AN230/AN231/AN232/AN233/AN234/AN235/AN236/AN237/AN238/AN239/AN240/AN241/AN242/AN243/AN244/AN245/AN246/AN247/AN248/AN249/AN250/AN251/AN252/AN253/AN254/AN255/AN256/AN257/AN258/AN259/AN260/AN261/AN262/AN263/AN264/AN265/AN266/AN267/AN268/AN269/AN270/AN271/AN272/AN273/AN274/AN275/AN276/AN277/AN278/AN279/AN280/AN281/AN282/AN283/AN284/AN285/AN286/AN287/AN288/AN289/AN290/AN291/AN292/AN293/AN294/AN295/AN296/AN297/AN298/AN299/AN300/AN301/AN302/AN303/AN304/AN305/AN306/AN307/AN308/AN309/AN310/AN311/AN312/AN313/AN314/AN315/AN316/AN317/AN318/AN319/AN320/AN321/AN322/AN323/AN324/AN325/AN326/AN327/AN328/AN329/AN330/AN331/AN332/AN333/AN334/AN335/AN336/AN337/AN338/AN339/AN340/AN341/AN342/AN343/AN344/AN345/AN346/AN347/AN348/AN349/AN350/AN351/AN352/AN353/AN354/AN355/AN356/AN357/AN358/AN359/AN360/AN361/AN362/AN363/AN364/AN365/AN366/AN367/AN368/AN369/AN370/AN371/AN372/AN373/AN374/AN375/AN376/AN377/AN378/AN379/AN380/AN381/AN382/AN383/AN384/AN385/AN386/AN387/AN388/AN389/AN390/AN391/AN392/AN393/AN394/AN395/AN396/AN397/AN398/AN399/AN400/AN401/AN402/AN403/AN404/AN405/AN406/AN407/AN408/AN409/AN410/AN411/AN412/AN413/AN414/AN415/AN416/AN417/AN418/AN419/AN420/AN421/AN422/AN423/AN424/AN425/AN426/AN427/AN428/AN429/AN430/AN431/AN432/AN433/AN434/AN435/AN436/AN437/AN438/AN439/AN440/AN441/AN442/AN443/AN444/AN445/AN446/AN447/AN448/AN449/AN450/AN451/AN452/AN453/AN454/AN455/AN456/AN457/AN458/AN459/AN460/AN461/AN462/AN463/AN464/AN465/AN466/AN467/AN468/AN469/AN470/AN471/AN472/AN473/AN474/AN475/AN476/AN477/AN478/AN479/AN480/AN481/AN482/AN483/AN484/AN485/AN486/AN487/AN488/AN489/AN490/AN491/AN492/AN493/AN494/AN495/AN496/AN497/AN498/AN499/AN500/AN501/AN502/AN503/AN504/AN505/AN506/AN507/AN508/AN509/AN510/AN511/AN512/AN513/AN514/AN515/AN516/AN517/AN518/AN519/AN520/AN521/AN522/AN523/AN524/AN525/AN526/AN527/AN528/AN529/AN530/AN531/AN532/AN533/AN534/AN535/AN536/AN537/AN538/AN539/AN540/AN541/AN542/AN543/AN544/AN545/AN546/AN547/AN548/AN549/AN550/AN551/AN552/AN553/AN554/AN555/AN556/AN557/AN558/AN559/AN560/AN561/AN562/AN563/AN564/AN565/AN566/AN567/AN568/AN569/AN570/AN571/AN572/AN573/AN574/AN575/AN576/AN577/AN578/AN579/AN580/AN581/AN582/AN583/AN584/AN585/AN586/AN587/AN588/AN589/AN590/AN591/AN592/AN593/AN594/AN595/AN596/AN597/AN598/AN599/AN600/AN601/AN602/AN603/AN604/AN605/AN606/AN607/AN608/AN609/AN610/AN611/AN612/AN613/AN614/AN615/AN616/AN617/AN618/AN619/AN620/AN621/AN622/AN623/AN624/AN625/AN626/AN627/AN628/AN629/AN630/AN631/AN632/AN633/AN634/AN635/AN636/AN637/AN638/AN639/AN640/AN641/AN642/AN643/AN644/AN645/AN646/AN647/AN648/AN649/AN650/AN651/AN652/AN653/AN654/AN655/AN656/AN657/AN658/AN659/AN660/AN661/AN662/AN663/AN664/AN665/AN666/AN667/AN668/AN669/AN670/AN671/AN672/AN673/AN674/AN675/AN676/AN677/AN678/AN679/AN680/AN681/AN682/AN683/AN684/AN685/AN686/AN687/AN688/AN689/AN690/AN691/AN692/AN693/AN694/AN695/AN696/AN697/AN698/AN699/AN700/AN701/AN702/AN703/AN704/AN705/AN706/AN707/AN708/AN709/AN710/AN711/AN712/AN713/AN714/AN715/AN716/AN717/AN718/AN719/AN720/AN721/AN722/AN723/AN724/AN725/AN726/AN727/AN728/AN729/AN730/AN731/AN732/AN733/AN734/AN735/AN736/AN737/AN738/AN739/AN740/AN741/AN742/AN743/AN744/AN745/AN746/AN747/AN748/AN749/AN750/AN751/AN752/AN753/AN754/AN755/AN756/AN757/AN758/AN759/AN760/AN761/AN762/AN763/AN764/AN765/AN766/AN767/AN768/AN769/AN770/AN771/AN772/AN773/AN774/AN775/AN776/AN777/AN778/AN779/AN780/AN781/AN782/AN783/AN784/AN785/AN786/AN787/AN788/AN789/AN790/AN791/AN792/AN793/AN794/AN795/AN796/AN797/AN798/AN799/AN800/AN801/AN802/AN803/AN804/AN805/AN806/AN807/AN808/AN809/AN810/AN811/AN812/AN813/AN814/AN815/AN816/AN817/AN818/AN819/AN820/AN821/AN822/AN823/AN824/AN825/AN826/AN827/AN828/AN829/AN830/AN831/AN832/AN833/AN834/AN835/AN836/AN837/AN838/AN839/AN840/AN841/AN842/AN843/AN844/AN845/AN846/AN847/AN848/AN849/AN850/AN851/AN852/AN853/AN854/AN855/AN856/AN857/AN858/AN859/AN860/AN861/AN862/AN863/AN864/AN865/AN866/AN867/AN868/AN869/AN870/AN871/AN872/AN873/AN874/AN875/AN876/AN877/AN878/AN879/AN880/AN881/AN882/AN883/AN884/AN885/AN886/AN887/AN888/AN889/AN890/AN891/AN892/AN893/AN894/AN895/AN896/AN897/AN898/AN899/AN900/AN901/AN902/AN903/AN904/AN905/AN906/AN907/AN908/AN909/AN910/AN911/AN912/AN913/AN914/AN915/AN916/AN917/AN918/AN919/AN920/AN921/AN922/AN923/AN924/AN925/AN926/AN927/AN928/AN929/AN930/AN931/AN932/AN933/AN934/AN935/AN936/AN937/AN938/AN939/AN940/AN941/AN942/AN943/AN944/AN945/AN946/AN947/AN948/AN949/AN950/AN951/AN952/AN953/AN954/AN955/AN956/AN957/AN958/AN959/AN960/AN961/AN962/AN963/AN964/AN965/AN966/AN967/AN968/AN969/AN970/AN971/AN972/AN973/AN974/AN975/AN976/AN977/AN978/AN979/AN980/AN981/AN982/AN983/AN984/AN985/AN986/AN987/AN988/AN989/AN990/AN991/AN992/AN993/AN994/AN995/AN996/AN997/AN998/AN999/AN1000/AN1001/AN1002/AN1003/AN1004/AN1005/AN1006/AN1007/AN1008/AN1009/AN1010/AN1011/AN1012/AN1013/AN1014/AN1015/AN1016/AN1017/AN1018/AN1019/AN1020/AN1021/AN1022/AN1023/AN1024/AN1025/AN1026/AN1027/AN1028/AN1029/AN1030/AN1031/AN1032/AN1033/AN1034/AN1035/AN1036/AN1037/AN1038/AN1039/AN1040/AN1041/AN1042/AN1043/AN1044/AN1045/AN1046/AN1047/AN1048/AN1049/AN1050/AN1051/AN1052/AN1053/AN1054/AN1055/AN1056/AN1057/AN1058/AN1059/AN1060/AN1061/AN1062/AN1063/AN1064/AN1065/AN1066/AN1067/AN1068/AN1069/AN1070/AN1071/AN1072/AN1073/AN1074/AN1075/AN1076/AN1077/AN1078/AN1079/AN1080/AN1081/AN1082/AN1083/AN1084/AN1085/AN1086/AN1087/AN1088/AN1089/AN1090/AN1091/AN1092/AN1093/AN1094/AN1095/AN1096/AN1097/AN1098/AN1099/AN1100/AN1101/AN1102/AN1103/AN1104/AN1105/AN1106/AN1107/AN1108/AN1109/AN1110/AN1111/AN1112/AN1113/AN1114/AN1115/AN1116/AN1117/AN1118/AN1119/AN1120/AN1121/AN1122/AN1123/AN1124/AN1125/AN1126/AN1127/AN1128/AN1129/AN1130/AN1131/AN1132/AN1133/AN1134/AN1135/AN1136/AN1137/AN1138/AN1139/AN1140/AN1141/AN1142/AN1143/AN1144/AN1145/AN1146/AN1147/AN1148/AN1149/AN1150/AN1151/AN1152/AN1153/AN1154/AN1155/AN1156/AN1157/AN1158/AN1159/AN1160/AN1161/AN1162/AN1163/AN1164/AN1165/AN1166/AN1167/AN1168/AN1169/AN1170/AN1171/AN1172/AN1173/AN1174/AN1175/AN1176/AN1177/AN1178/AN1179/AN1180/AN1181/AN1182/AN1183/AN1184/AN1185/AN1186/AN1187/AN1188/AN1189/AN1190/AN1191/AN1192/AN1193/AN1194/AN1195/AN1196/AN1197/AN1198/AN1199/AN1200/AN1201/AN1202/AN1203/AN1204/AN1205/AN1206/AN1207/AN1208/AN1209/AN1210/AN1211/AN1212/AN1213/AN1214/AN1215/AN1216/AN1217/AN1218/AN1219/AN1220/AN1221/AN1222/AN1223/AN1224/AN1225/AN1226/AN1227/AN1228/AN1229/AN1230/AN1231/AN1232/AN1233/AN1234/AN1235/AN1236/AN1237/AN1238/AN1239/AN1240/AN1241/AN1242/AN1243/AN1244/AN1245/AN1246/AN1247/AN1248/AN1249/AN1250/AN1251/AN1252/AN1253/AN1254/AN1255/AN1256/AN1257/AN1258/AN1259/AN1260/AN1261/AN1262/AN1263/AN1264/AN1265/AN1266/AN1267/AN1268/AN1269/AN1270/AN1271/AN1272/AN1273/AN1274/AN1275/AN1276/AN1277/AN1278/AN1279/AN1280/AN1281/AN1282/AN1283/AN1284/AN1285/AN1286/AN1287/AN1288/AN1289/AN1290/AN1291/AN1292/AN1293/AN1294/AN1295/AN1296/AN1297/AN1298/AN1299/AN1300/AN1301/AN1302/AN1303/AN1304/AN1305/AN1306/AN1307/AN1308/AN1309/AN1310/AN1311/AN1312/AN1313/AN1314/AN1315/AN1316/AN1317/AN1318/AN1319/AN1320/AN1321/AN1322/AN1323/AN1324/AN1325/AN1326/AN1327/AN1328/AN1329/AN1330/AN1331/AN1332/AN1333/AN1334/AN1335/AN1336/AN1337/AN1338/AN1339/AN1340/AN1341/AN1342/AN1343/AN1344/AN1345/AN1346/AN1347/AN1348/AN1349/AN1350/AN1351/AN1352/AN1353/AN1354/AN1355/AN1356/AN1357/AN1358/AN1359/AN1360/AN1361/AN1362/AN1363/AN1364/AN1365/AN1366/AN1367/AN1368/AN1369/AN1370/AN1371/AN1372/AN1373/AN1374/AN1375/AN1376/AN1377/AN1378/AN1379/AN1380/AN1381/AN1382/AN1383/AN1384/AN1385/AN1386/AN1387/AN1388/AN1389/AN1390/AN1391/AN1392/AN1393/AN1394/AN1395/AN1396/AN1397/AN1398/AN1399/AN1400/AN1401/AN1402/AN1403/AN1404/AN1405/AN1406/AN1407/AN1408/AN1409/AN1410/AN1411/AN1412/AN1413/AN1414/AN1415/AN1416/AN1417/AN1418/AN1419/AN1420/AN1421/AN1422/AN1423/AN1424/AN1425/AN1426/AN1427/AN1428/AN1429/AN1430/AN1431/AN1432/AN1433/AN1434/AN1435/AN1436/AN1437/AN1438/AN1439/AN1440/AN1441/AN1442/AN1443/AN1444/AN1445/AN1446/AN1447/AN1448/AN1449/AN1450/AN1451/AN1452/AN1453/AN1454/AN1455/AN1456/AN1457/AN1458/AN1459/AN1460/AN1461/AN1462/AN1463/AN1464/AN1465/AN1466/AN1467/AN1468/AN1469/AN1470/AN1471/AN1472/AN1473/AN1474/AN1475/AN1476/AN1477/AN1478/AN1479/AN1480/AN1481/AN1482/AN1483/AN1484/AN1485/AN1486/AN1487/AN1488/AN1489/AN1490/AN1491/AN1492/AN1493/AN1494/AN1495/AN1496/AN1497/AN1498/AN1499/AN1500/AN1501/AN1502/AN1503/AN1504/AN1505/AN1506/AN1507/AN1508/AN1509/AN1510/AN1511/AN1512/AN1513/AN1514/AN1515/AN1516/AN1517/AN1518/AN1519/AN1520/AN1521/AN1522/AN1523/AN1524/AN1525/AN1526/AN1527/AN1528/AN1529/AN1530/AN1531/AN1532/AN1533/AN1534/AN1535/AN1536/AN1537/AN1538/AN1539/AN1540/AN1541/AN1542/AN1543/AN1544/AN1545/AN1546/AN1547/AN1548/AN1549/AN1550/AN1551/AN1552/AN1553/AN1554/AN1555/AN1556/AN1557/AN1558/AN1559/AN1560/AN1561/AN1562/AN1563/AN1564/AN1565/AN1566/AN1567/AN1568/AN1569/AN1570/AN1571/AN1572/AN1573/AN1574/AN1575/AN1576/AN1577/AN1578/AN1579/AN1580/AN1581/AN1582/AN1583/AN1584/AN1585/AN1586/AN1587/AN1588/AN1589/AN1590/AN1591/AN1592/AN1593/AN1594/AN1595/AN1596/AN1597/AN1598/AN1599/AN1600/AN1601/AN1602/AN1603/AN1604/AN1605/AN1606/AN1607/AN1608/AN1609/AN1610/AN1611/AN1612/AN1613/AN1614/AN1615/AN1616/AN1617/AN1618/AN1619/AN1620/AN1621/AN1622/AN1623/AN1624/AN1625/AN1626/AN1627/AN1628/AN1629/AN1630/AN1631/AN1632/AN1633/AN1634/AN1635/AN1636/AN1637/AN1638/AN1639/AN1640/AN1641/AN1642/AN1643/AN1644/AN1645/AN1646/AN1647/AN1648/AN1649/AN1650/AN1651/AN1652/AN1653/AN1654/AN1655/AN1656/AN1657/AN1658/AN1659/AN1660/AN1661/AN1662/AN1663/AN1664/AN1665/AN1666/AN1667/AN1668/AN1669/AN1670/AN1671/AN1672/AN1673/AN1674/AN1675/AN1676/AN1677/AN1678/AN1679/AN1680/AN1681/AN1682/AN1683/AN1684/AN1685/AN1686/AN1687/AN1688/AN1689/AN1690/AN1691/AN1692/AN1693/AN1694/AN1695/AN1696/AN1697/AN1698/AN1699/AN1700/AN1701/AN1702/AN1703/AN1704/AN1705/AN1706/AN1707/AN1708/AN1709/AN1710/AN1711/AN1712/AN1713/AN1714/AN1715/AN1716/AN1717/AN1718/AN1719/AN1720/AN1721/AN1722/AN1723/AN1724/AN1725/AN1726/AN1727/AN1728/AN1729/AN1730/AN1731/AN1732/AN1733/AN1734/AN1735/AN1736/AN1737/AN1738/AN1739/AN1740/AN1741/AN1742/AN1743/AN1744/AN1745/AN1746/AN1747/AN1748/AN1749/AN1750/AN1751/AN1752/AN1753/AN1754/AN1755/AN1756/AN1757/AN1758/AN1759/AN1760/AN1761/AN1762/AN1763/AN1764/AN1765/AN1766/AN1767/AN1768/AN1769/AN1770/AN1771/AN1772/AN1773/AN1774/AN1775/AN1776/AN1777/AN1778/AN1779/AN1780/AN1781/AN1782/AN1783/AN1784/AN1785/AN1786/AN1787/AN1788/AN1789/AN1790/AN1791/AN1792/AN1793/AN1794/AN1795/AN1796/AN1797/AN1798/AN1799/AN1800/AN1801/AN1802/AN1803/AN1804/AN1805/AN1806/AN1807/AN1808/AN1809/AN1810/AN1811/AN1812/AN1813/AN1814/AN1815/AN1816/AN1817/AN1818/AN1819/AN1820/AN1821/AN1822/AN1823/AN1824/AN1825/AN1826/AN1827/AN1828/AN1829/AN1830/AN1831/AN1832/AN1833/AN1834/AN1835/AN1836/AN1837/AN1838/AN1839/AN1840/AN1841/AN1842/AN1843/AN1844/AN1845/AN1846/AN1847/AN1848/AN1849/AN1850/AN1851/AN1852/AN1853/AN1854/AN1855/AN1856/AN1857/AN1858/AN1859/AN1860/AN1861/AN1862/AN1863/AN1864/AN1865/AN1866/AN1867/AN1868/AN1869/AN1870/AN1871/AN1872/AN1873/AN1874/AN1875/AN1876/AN1877/AN1878/AN1879/AN1880/AN1881/AN1882/AN1883/AN1884/AN1885/AN1886/AN1887/AN1888/AN1889/AN1890/AN1891/AN1892/AN1893/AN1894/AN1895/AN1896/AN1897/AN1898/AN1899/AN1900/AN1901/AN1902/AN1903/AN1904/AN1905/AN1906/AN1907/AN1908/AN1909/AN1910/AN1911/AN1912/AN1913/AN1914/AN1915/AN1916/AN1917/AN1918/AN1919/AN1920/AN1921/AN1922/AN1923/AN1924/AN1925/AN1926/AN1927/AN1928/AN1929/AN1930/AN1931/AN1932/AN1933/AN1934/AN1935/AN1936/AN1937/AN1938/AN1939/AN1940/AN1941/AN1942/AN1943/AN1944/AN1945/AN1946/AN1947/AN1948/AN1949/AN1950/AN1951/AN1952/AN1953/AN1954/AN1955/AN1956/AN1957/AN1958/AN1959/AN1960/AN1961/AN1962/AN1963/AN1964/AN1965/AN1966/AN1967/AN1968/AN1969/AN1970/AN1971/AN1972/AN1973/AN1974/AN1975/AN1976/AN1977/AN1978/AN1979/AN1980/AN1981/AN1982/AN1983/AN1984/AN1985/AN1986/AN1987/AN1988/AN1989/AN1990/AN1991/AN1992/AN1993/AN1994/AN1995/AN1996/AN1997/AN1998/AN1999/AN2000/AN2001/AN2002/AN2003/AN2004/AN2005/AN2006/AN2007/AN2008/AN2009/AN2010/AN2011/AN2012/AN2013/AN2014/AN2015/AN2016/AN2017/AN2018/AN2019/AN2020/AN2021/AN2022/AN2023/AN2024/AN2025/AN2026/AN2027/AN2028/AN2029/AN2030/AN2031/AN2032/AN2033/AN2034/AN2035/AN2036/AN2037/AN2038/AN2039/AN2040/AN2041/AN2042/AN2043/AN2044/AN2045/AN2046/AN2047/AN2048/AN2049/AN2050/AN2051/AN2052/AN2053/AN2054/AN2055/AN2056/AN2057/AN2058/AN2059/AN2060/AN2061/AN2062/AN2063/AN2064/AN2065/AN2066/AN2067/AN2068/AN2069/AN2070/AN2071/AN2072/AN2073/AN2074/AN2075/AN2076/AN2077/AN2078/AN2079/AN2080/AN2081/AN2082/AN2083/AN2084/AN2085/AN2086/AN2087/AN2088/AN2089/AN2090/AN2091/AN2092/AN2093/AN2094/AN2095/AN2096/AN2097/AN2098/AN2099/AN2100/AN2101/AN2102/AN2103/AN2104/AN2105/AN2106/AN2107/AN2108/AN2109/AN2110/AN21	

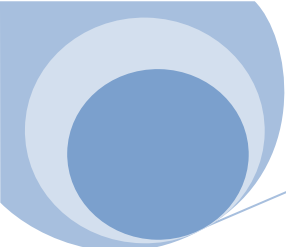






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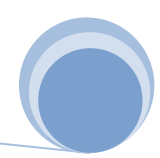
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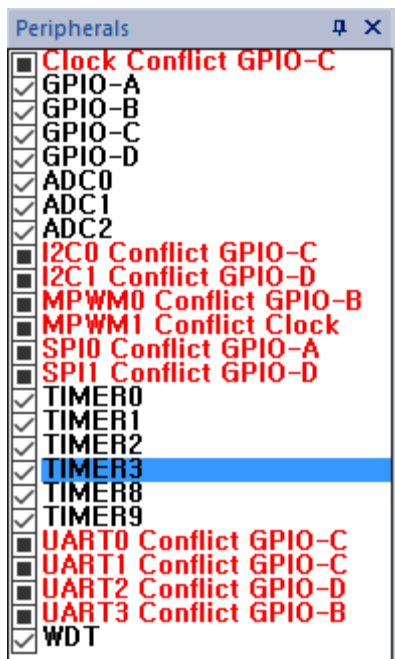
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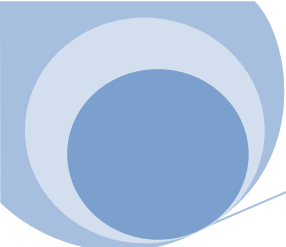
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Properties

Application

Control #1 reg.

ADC trigger so...	Disable
Initial output va...	Low
Clock source	PCLK/2
Clear capture at	Rising edge
Timer mode	Periodic

Prescaler reg.

divisor(1~64)	1
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General A reg.

Period (ms)	0.004000
PWM duty (ms)	0.004000
OneShot delay ...	0.004000

[Empty text]

[Empty text]

Properties

Application

INT. enable reg.

Line control reg.

Data control reg.

Baudrate, Time

Baudrate	9600
Wait value	0

Properties

Application

INT. enable reg.

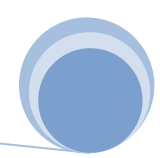
Line control reg.

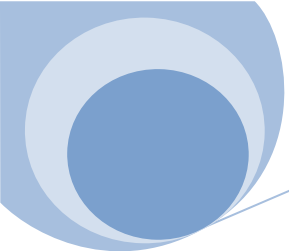
Data control reg.

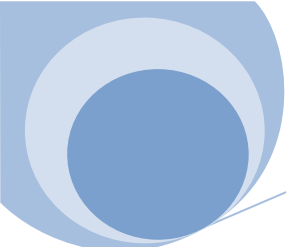
Baudrate, Time

Baudrate	9600.129233
Wait value	0

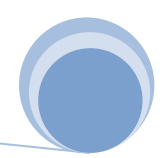
[Empty text]







```
Output
Message count is 36
Clock : Set Xin(PC12) as Analog port
Clock : Select Xout(PC13) port
I2C0 : SDA0 is not selected
I2C0 : SCL0 is not selected
I2C1 : SDA1 is not selected
I2C1 : SCL1 is not selected
Error
```



ABOV 32bit : A3x series code generator

AC33M8128L : 80 LQFP (project file = AC33M8128L_80LQ.VPR)

Peripherals:

- GPIO-A
- GPIO-B
- GPIO-C
- GPIO-D
- ADC0
- ADC1
- ADC2
- I2C0 Conflict GPIO-C
- I2C1 Conflict GPIO-D
- MPWM0 Conflict GPIO-B
- MPWM1 Conflict Clock
- SP10 Conflict GPIO-A
- SP11 Conflict GPIO-D
- TIMER0
- TIMER1
- TIMER2
- TIMER3
- TIMER8
- TIMER9
- UART0 Conflict GPIO-C
- UART1 Conflict GPIO-C
- UART2 Conflict GPIO-B
- UART3 Conflict GPIO-B
- WDT

Properties:

- Application
- Internal OSC
 - SOSC 1MHz Use:x1
 - IOSC 20MHz Use:x1
- External OSC
 - MOSC Use:x1
 - Xin (MHz) 8.000000
- PLL OSC
 - PLL Use
 - PLL input MOSC
 - PLL (MHz) 64.000000
- MCLK OSC
 - MCLK input PLL output
 - MCLK (MHz) 64.000000
- system clock source (MCCR1)
 - Clock input MOSC
 - Clock divisor 4
 - Clock (MHz) 2.000000
- Trace clock (MCCR1)
 - Clock input IOSC
 - Clock divisor 1
 - Clock (MHz) 20.000000
- MPWM0 clock (MCCR2)
 - Clock input SOSC

Package: C33M8128L, Package : 80LQFP

peric.c:

```

// Used ABOV Semiconductor code generator
// Initialize each peripherals
//-----
#include "init.h"
#include "peri.h"
//-----
void Init_GPIO(void)
{
    PORT_ACCESS_EN();

    // GPIO-A -----
    SCU->PER1 |= 0x00000100;
    SCU->PCER1 |= 0x00000100;

    PCA->MR = 0x50000000;
    PCA->CR = 0x00000000;
    PCA->PCR = 0x00000001;
    PCA->DER = 0x00000000;
    PCA->IER = 0x00000004;
    PCA->ICR = 0x00000004;

    NVIC_SetPriority(GPIOAQ_IRQn, ((0x01<<1)|0x01));
    NVIC_EnableIRQ(GPIOAQ_IRQn);
    __enable_irq();

    // GPIO-B -----
    SCU->PER1 |= 0x00000200;
    SCU->PCER1 |= 0x00000200;

    PCB->MR = 0x00000000;
    PCB->CR = 0x00000000;
    
```

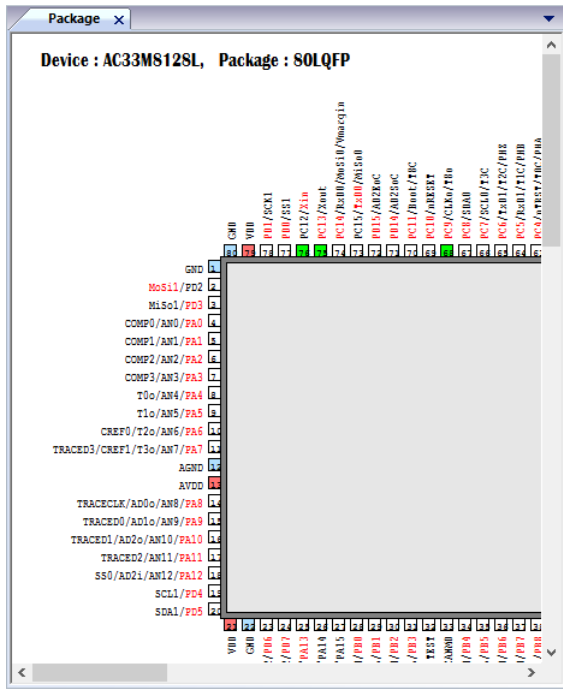
Output:


```

Message count is 35
Clock : Set Xin(PC12) as Analog port
Clock : Select Xout(PC13) port
I2C0 : SDA0 is not selected
I2C0 : SCL0 is not selected
I2C1 : SDA1 is not selected
I2C1 : SCL1 is not selected
    
```

Pane 1 Error

Pane 2



Pin5 : PA1/AN1/COMP1

PA1




```
peri.c x
//=====
// Used ABOV Semiconductor code generator
// Initialize each peripherals
//=====
#include "init.h"
#include "peri.h"

//-----
void init_GPIO(void)
{
    PORT_ACCESS_EN();

    // GPIO-A -----
    SCU->PER1 |= 0x00000100;
    SCU->PCER1 |= 0x00000100;

    PCA->MR = 0x50000000;
    PCA->CR = 0x00000000;
    PCA->PCR = 0x00000001;
    PCA->DER = 0x00000000;
    PCA->IER = 0x00000004;
    PCA->ICR = 0x00000004;

    NVIC_SetPriority(GPIOAQ_IRQn, ((0x01<<1)|0x01));
    NVIC_EnableIRQ(GPIOAQ_IRQn);
    __enable_irq();

    // GPIO-B -----
    SCU->PER1 |= 0x00000200;
    SCU->PCER1 |= 0x00000200;

    PCB->MR = 0x00000000;
    PCB->CR = 0x00000000.
```

```
ABOV_VPE32_LIC.txt x
NOTICE and LICENSE of ABOV Semiconductor VPE32 (ARM code gene

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http://www.abov.co.kr

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Developer :

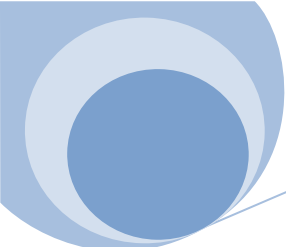
- ABOV VPE32 software : Seungduk Ha
- Debugging : Sungje Park
```



```
//=====
// Used ABOV Semiconductor code generator
// Device name : AC33M8128
//=====
#include "AC33Mx128.h"
#include "system_AC33Mx128.h"
#include "aa_types.h"
#include "ac33mx128_adc.h"
#include "ac33mx128_afe.h"
#include "ac33mx128_dmac.h"
#include "ac33mx128_gpio.h"
#include "ac33mx128_i2c.h"
#include "ac33mx128_libcfg.h"
#include "ac33mx128_mpwm.h"
#include "ac33mx128_pcu.h"
#include "ac33mx128_pwr.h"
#include "ac33mx128_scu.h"
#include "ac33mx128_spi.h"
#include "ac33mx128_timer.h"
#include "ac33mx128_uart.h"
#include "ac33mx128_wdt.h"
#include "debug_frmwrk.h"
#include "slib.h"


#define _ADC
#define _AFE
#define _DMAC
#define _GPIO
#define _I2C
#define _MPWM
#define _SPI
#define _TIMER
#define _UART
#define _WDT

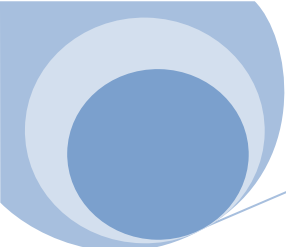
void init(void);
```



```
//=====
// Used ABOV Semiconductor code generator
// Define initialize function of each peripherals
//=====


void init_GPIO(void);
void GPIOA_IRQHandler(void);
void GPIOCE_IRQHandler(void);
void init_clock(void);
void init_ADC_0(void);
void init_ADC_1(void);
void init_ADC_2(void);
void init_I2C_0(void);
void init_I2C_1(void);
void init_MPWM_0(void);
void init_MPWM_1(void);
void init_SPI_0(void);
void init_SPI_1(void);
void init_TIMER_0(void);
void init_TIMER_1(void);
void init_TIMER_2(void);
void init_TIMER_3(void);
void init_TIMER_8(void);
void init_TIMER_9(void);
void init_UART_0(void);
void init_UART_1(void);
void init_UART_2(void);
void init_UART_3(void);
void init_WDT(void);
```

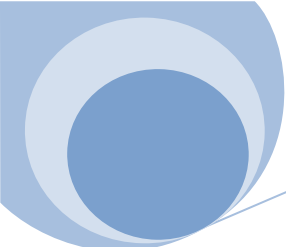




```
//=====
// Used ABOV Semiconductor code generator
// Basic initialize function
//=====
#include "init.h"
#include "peri.h"

void init(void)
{
    init_GPIO();
    init_clock();
    init_ADC_0();
    init_ADC_1();
    init_ADC_2();
    init_I2C_0();
    init_I2C_1();
    init_MFWM_0();
    init_MFWM_1();
    init_SPI_0();
    init_SPI_1();
    init_TIMER_0();
    init_TIMER_1();
    init_TIMER_2();
    init_TIMER_3();
    init_TIMER_8();
    init_TIMER_9();
    init_UART_0();
    init_UART_1();
    init_UART_2();
    init_UART_3();
    init_WDT();
}
```





```
//=====
// Used ABOV Semiconductor code generator
// Initialize each peripherals
//=====
#include "init.h"
#include "peri.h"


//-----
void init_GPIO(void)
{
    PORT_ACCESS_EN();


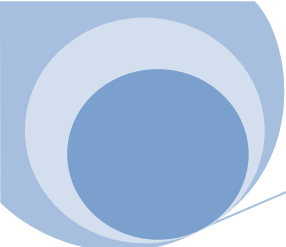
    // GPIO-A -----
    SCU->PER1 |= 0x00000100;
    SCU->PCER1 |= 0x00000100;








    PCA->MR = 0x50000000;
    PCA->CR = 0x00000000;
    PCA->PCR = 0x00000001;
    PCA->DER = 0x00000000;
    PCA->IER = 0x00000004;
    PCA->ICR = 0x00000004;

    NVIC_SetPriority(GPIOA0_IRQn, ((0x01<<1)|0x01));
    NVIC_EnableIRQ(GPIOA0_IRQn);
    __enable_irq();

    // GPIO-B -----
    SCU->PER1 |= 0x00000200;
    SCU->PCER1 |= 0x00000200;
```





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 SVD	2016-06-23 오전 9:33



End of document.

